ORIGINALLY FILED

Docket No. 740819-425
K OFFICE 9/26/07

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re l	Patent Application of:)	* *	71	0	// ·
	to INOUE et al.)	Group Art Unit: 2811		4	m
Serial	No. 09/666,156)	Examiner: Steven Ho Yin L	oke	,	··.
iled:	September 19, 2000).				
or:	HIGH-VOLTAGE MOS TRANSISTOR (AS AMENDED))	Date: September 6, 2002			
CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 2023, on September 6, 2002. Jeannie M. Saxton						l in

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated May 8, 2002, the period for response having been extended one (1) month until September 8, 2002, the following amendment and remarks are respectfully submitted in connection with the above-identified application.

IN THE TITLE OF THE INVENTION:

Please delete the title of the invention and substitute therefor "HIGH-VOLTAGE MOS TRANSISTOR."

IN THE DRAWINGS:

Please consider the Request for Approval of Drawing Corrections concurrently filed NVA239062.1